6/9/23, 10:18 AM Action History

ACTION HISTORY OF RTI REQUEST No.DITEC/R/T/22/00133/2

Jeeva Dharshini Uk **Applicant Name**

Transferred u/s 6(3) of RTI Act, 2005 for information related to Early **Text of Application**

Harvest Programmes under Digital India.

Reply of Application the information w.r.t. HRD Division, MeitY is attached.

SN.	Action Taken	Date of Action	Action Taken By	Remarks
1	RTI REQUEST RECEIVED	06/04/2022	Nodal Officer	DITEC/R/T/22/00133
2	REQUEST FORWARDED TO CPIO	06/04/2022	Nodal Officer	Forwarded to CPIO(s):
3	REQUEST DISPOSED OF	19/04/2022	Surendra Singh(HRD)- (CPIO)	
			Print	

No. L-14015/1/2021-HRD Government of India Ministry of Electronics and Information Technology HRD Division

Dated: 18.04.2022

Subject:- Reference RTI Application of Smt. Jeeva Dharshini UK under RTI Act, 2005

Reference RTI Application received from Smt. Jeeva Dharshini UK dated 5.4.2022 received from Sci'D' (SS) & CPIO – HRD Division vide communication No. 3(2)2011-HRD (Vol.VI) dated 07.04.2022 under RTI Act, 2005.

- 2. The inputs in r/o HRD Division is enclosed herewith.
- 3. This issues with the approval of HoD(HRD).

(Sonia Singh) Section Officer I.com No. 849

Sci'D' (SS) & CPIO

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(Government of India) Ministry of Electronics and Information Technology (MeitY) HRD activities

To ensure availability of trained human resources for the manufacturing and service in electronics and ICT Sector for building digitally empowered, knowledge based and inclusive society.

HRD activities of MeitY are targeted to ensure availability of trained human resources for the manufacturing & service sectors of electronics and IT industry. Initiatives include identifying gaps emerging from the formal sector and planning programmes in non-formal and formal sectors for meeting these gaps. This includes Skill Development in the domain of Electronics & IT and related areas. The Skill Development activities of the Ministry are primarily being taken up by its two autonomous societies viz. National Institute of Electronics and Information Technology (NIELIT) and Centre for Development of Advanced Computing (C-DAC). NIELIT has forty three (43) centers across the country and providing training in Formal & Non-Formal Education in the area of IECT besides development of industry oriented quality education and training programmes in the state-of-the-art areas. CDAC has (12) twelve centers across the country. In addition, the various organizations / attached offices under the Department viz. ERNET India, Media Lab Asia, CSC E-Governance Services India Limited, STQC, NIC etc. are also engaged in training of various stakeholders in small numbers.

The following schemes/activities pertaining to Human Resource Development for Electronics and ICT sector have been approved/under implementation / being evolved:-

- i. Visvesvaraya PhD Scheme for Electronics and IT: This scheme provides support for 3000PhDs (1000 Full Time & 2000 part Time), to promote innovation and development of new products in IT/ITES and ESDM sectors. So far, a total of 1154 PhD (968 full time + 186 part time) candidates have been enrolled at the institutes.
- ii. Scheme of Financial Assistance for setting up of Electronics and ICT Academies: Under this scheme 07(Seven) E&ICT Academies have been setup for faculty/mentor development/up gradation to improve the employability of the graduates/diploma holders. Under the scheme so far, 3,29,855 beneficiaries have been trained under 1,774 faculty development programmes (Faculty: 1,02,173; Students/ Others: 2,27,682) by the academies.
- iii. FutureSkills 'PRIME': MeitY in collaboration with IT/ITeS Sector Skills Council-NASSCOM has initiated a programme titled FutureSkills PRIME (Programme for Re-skilling/Up-skilling of IT Manpower for Employability). FutureSkills PRIME is an 'aggregator of aggregators' platform comprising various online skills providers to provide digital skills training on a national scale in online mode. The programme is aimed at re-skilling/ up-skilling of IT professionals in 10 new/emerging technologies namely Artificial Intelligence, Big Data Analytics, Robotic Process Automation, Additive Manufacturing/ 3D Printing, Cloud Computing, Social & Mobile, Cyber Security, Augmented/Virtual Reality, Internet of Things and Blockchain. Besides online mode, 40 centres of CDAC and NIELIT are also implementing the Blended Learning mode, Training of Trainers and Government Official training programmes. The programme aims to cover a target of 4.12 Lakh beneficiaries. On the FutureSkills PRIME portal, so far more than 7 Lakh candidates have signed-up. A total of 3.46 Lakh candidates have been enrolled in aligned and non-aligned courses, out of which 1.21 Lakh candidates have completed the courses. Under the blended-learning programme, so far, 524 trainers and 4,292 Government officials have been trained.
- iv. Information Security Education and Awareness (ISEA) Project Phase-II: This project aims at capacity building in Information Security, training of Government personnel and creation of mass Information Security awareness. So far, 78,315 candidates have been trained/under-going training in various formal/non-formal courses in Information Security through 52 institutions (further, 5 Technical

Universities participating under the project have reported around 2.74 lakh candidates as trained/under-going training in formal courses in their respective affiliated colleges). In addition, 23,618 Government officials have been trained in various short term courses through direct trainings/e-Learning mode. Besides this, 1,378 awareness workshops have been conducted across the country covering 2,58,631 participants and around 5.75 crore (estimated) beneficiaries have been covered under indirect mode.

- v. Special Manpower Development Programmes (SMDP) for VLSI and Chip Design: The aim of the project is to train 50,000 number of specialized manpower in the area of VLSI design and inculcate the culture of System-on-Chip/System Level Design at Bachelors, Masters and Research level at 60 academic/Research & Development institutions spread across the country including IITs, NITs, IISc, IIITs & other institutions. Under the project, about 40,000 manpower specialized in VLSI/ System design area trained at B.Tech, M.Tech and PhD level, implementation of 15 projects for development of working prototype of Systems/ Sub-Systems/ SoCs are in progress, State-of-the-art VLSI design labs have been setup at 60 PIs equipped with Hardware platforms and EDA Tools to undertake VLSI chips/systems design as well as to train manpower.
- vi. Skill Development in ESDM sector: The Government has approved two schemes for Skill Development in ESDM Sector viz. (i) "Scheme for Financial Assistance to select States/UTs for Skill Development in Electronics System Design and Manufacturing (ESDM) sector" (Scheme-1) and (ii) "Skill Development in ESDM for Digital India" (Scheme-2) to facilitate creation of an eco-system for development of ESDM Sector in the entire country. Both the Schemes are implemented through Training Partners (TPs) affiliated with Key Implementing Agencies Electronics Sector Skill Council of India (ESSCI), Telecom Sector Skill Council (TSSC), National Institute of Electronics & IT (NIELIT) and Healthcare Sector Skill Council (HSSC). Both the above Schemes are being implement concurrently. Under both schemes so far, a total of 4,26,851 candidates have been enrolled out of which 4,20,513 candidates have been trained, and 2,97,989 candidates have certified.
- vii. Digital Literacy to the masses in the Country: Under two schemes viz. NDLM and DISHA a total of 53.7 lakhs person were trained & certified in Digital Literacy. The Government has approved a new scheme titled "Pradhan Mantri Gramin Digital Saksharta Abhiyan (PMGDISHA)" to usher in digital literacy in rural India by covering 6 crore rural households. Under the PMGDISHA so far, a total of 5.81 crore candidates have been enrolled and 4.92 crore have been trained, out of which 3.65 crore candidates have been certified.
- viii. Create skill development facilities in deprived areas through strengthening of NIELIT: The objective of the project is to Upgrade & newly setting up of 18 NIELIT Centres/extension centres for imparting training in various Electronics & ICT courses in North East region. Total 18 NIELIT Centres/extension centres have been established/made operational under the project and around 1,92,842 candidates have been trained in various Electronics & ICT courses so far. Additionally, two more NIELIT center (with campus facilities) has already been set up at Kohima and Agartala.
- ix. Technology Mediated Delivery of Courses in Medical Science Education for NER Medical Colleges: The objectives of the project is to assist and supplement the medical colleges of primarily the North East, in providing expert medical education and continued classes through technology mediated education by Virtual teaching through the online learning platform of CDAC Noida in collaboration with AIIMS, New Delhi. In the project AIIMS, New Delhi is the Knowledge provider and CDAC Noida is Technology provider. Under the project so far, the virtual teaching assistance has been provided to 2034 students of various NER medical colleges.
- x. Establishment of intelligent educational Infrastructure (Smart) in EklavyaModel Residential Schools (EMRSs): MeitY has approved the project titled "Establishment of intelligent educational Infrastructure (Smart) in Eklavya Model Residential Schools (EMRSs)" over a period of 2 years. The

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major objective of the of the proposal to setting up of intelligent educational Infrastructure (smart) in Eklavya Model Residential Schools(EMRSs)by creating eco system using latest tools and technologies, which helps to improve the learning outcome and to provide the internet connectivity.

xi. Setting up ICT Infrastructure in Government School familiarized usage of personalized adaptive learning: MeitY has approved the project titled "Setting up ICT Infrastructure in Government School familiarized usage of personalized adaptive learning" over a period of eighteen months with total budget outlay of Rs. 646.09 Lakhs. The major objective of the of the proposal to set up the advanced Digital ICT lab Infrastructure in 40 Government School in 8 North East States for creating eco system by using latest tools and technologies along with internet connectivity, to provide training to 400 teachers and also to conduct workshop.
