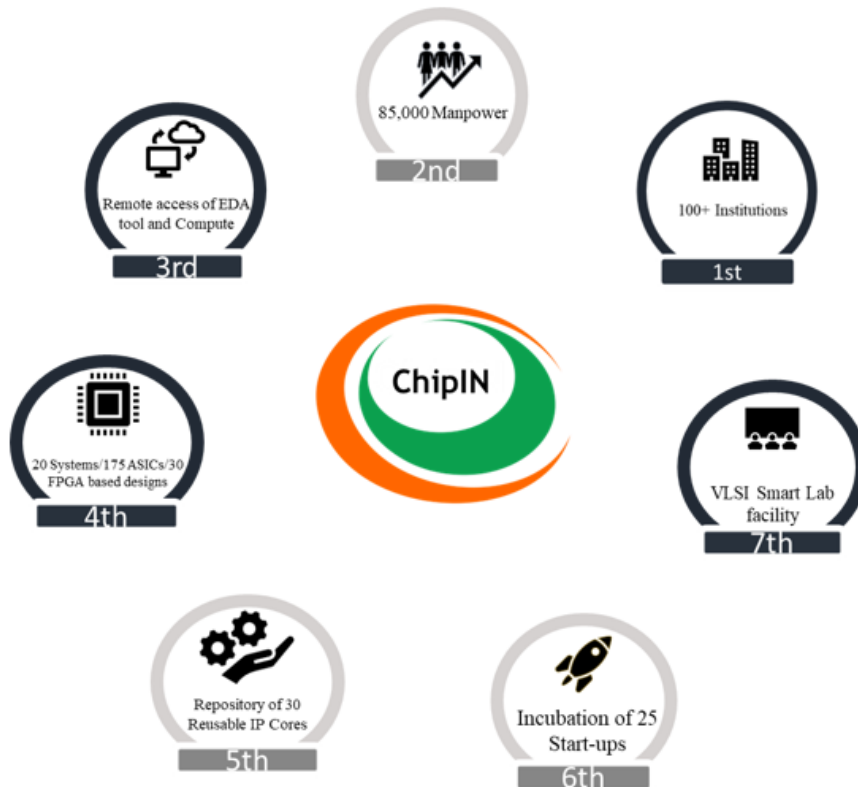


02nd Call for Proposal under Chips to Start-up (C2S) programme



Ministry of Electronics and IT has announced the Chips to Start-up (C2S) Programme with an aim to generate industry-ready 85,000 number of manpower specialized in the area of VLSI/ Chip design and Embedded System Design at B.Tech, M.Tech & PhD level at 100+ organizations (including Academia, R&D Organizations, Start-up & MSME) over a period of 5 years and act as catalyst for growth of domestic Start-ups involved in fabless chip design. The details are at <https://c2s.gov.in/>



The list of 103 organizations selected earlier under C2S Programme for financial support as well as chip design (EDA tools from SNPS, CDN, Siemens EDA, Ansys etc) & fabrication (MPW, NRE etc) support is at https://c2s.gov.in/list_of_proposals_selected_under_c2s.jsp

The Academic intuitions and R&D organizations other than these 103 organizations are now invited for project submission in Category-II & III of C2S Programme from 15th August - 15th September 2023. Details at <https://c2s.gov.in/callforproposals.jsp>